
Welcome to

CP/PC220 – Digital Electronics

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Introduction to digital logic: logic gates; combinational circuit analysis using Boolean algebra and Karnaugh maps; number systems and codes; minimization techniques applied to combinational logic systems; flip-flops, multi vibrators, counters and shift registers. (Cross-listed as CP 220)

Instructor:

Name: Shaowen Song

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Office Hours: To be discussed

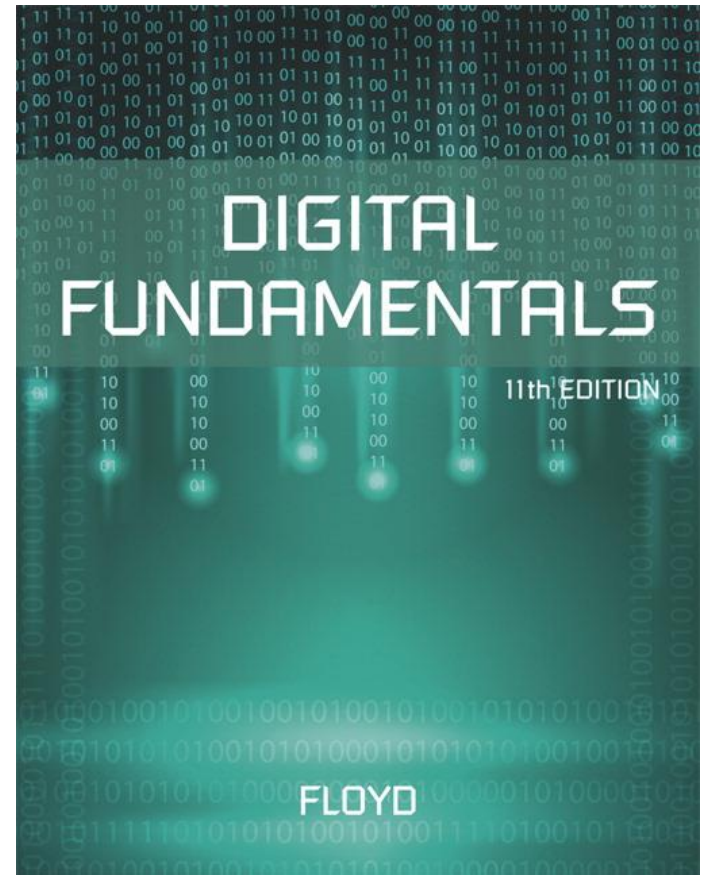
Textbook:

Textbook:

Digital Fundamentals – 11th Edition

Thomas L. Floyd

Pearson



Lecture Schedule:

2 lectures (1.5h each) per week

| Section | Days | Times | Room | Instructor |
|---------|------|------------------|------|---|
| Lecture | MW | 2:30 pm - 3:50pm | | <u>Dr. Shaowen Song</u> |

Labs:

1.5 lab hours

| Section | Days | Times | Room | Instructor |
|---------|------|-------|------|------------|
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Grading:

- Lab: 20%
- Assignments: 15%
- Midterm Exam: 20%
- Final Exam: 45%

Tentative Schedule

| Time | Lecture # | | Assignment | Assignment Due date |
|--|------------------------------|---|---|-------------------------------|
| Sept. 9 (M) | #1-A #1-B | Introduction to PC/CP220 Chapter 1: Introductory Concepts (Introduction) | No Assignment | |
| Sept. 11 (W) Sept. 16 (M) Sept. 18 (W) | #2-A #2-B #2-C | Chapter 2: Number System, Operations, and Codes | Assignment #1 (Chapter 2) | Sept.. 23 (M) |
| Sept. 23 (M) Sept. 25 (W) | #3-A #3-B | Chapter 3: Logic Gates | Assignment #2 (Chapter 3) | Sept.. 30 (M) |
| Sept. 30 (M) Oct. 2 (W) Oct. 7 (M) Oct. 9 (W) | #4-A #4-B #4-C #4-D | Chapter 4: Boolean Algebra and Logic Simplification | Assignment #3-AB (Chapter 4) Assignment #3-CD | Oct. 7 (M) Not Hand in |
| Oct. 14 (M) Oct. 16 (W) | No Class | Oct. 14 (M) Thanksgiving Oct. 14 – 18 Reading week | | |
| Oct. 21 (M) Oct. 23 (W) | No classes In Class | Prepare for Midterm Midterm Exam (Chapters 1-4) | Assignment #4 (Chapter 5) | |
| Oct. 28 (M) Oct. 30 (W) | #5-A #5-B | Chapter 5: Combinational Logic Analysis | | Nov. 4 (M) |
| Nov. 4 (M) Nov. 6 (W) Nov. 11 (M) | #6-A #6-B #6-C | Chapter 6: Functions of combinational Logic | Assignment #5 (Chapter 6) | Not Hand in |
| Nov. 13 (W) Nov. 18 (M) | #7-A #7-B | Chapter 7: Latches, Flip-Flops and Timers | Assignment #6 (Chapter 7) | Nov. 20 (M) |
| Nov. 20 (W) Nov. 25 (M) | #8-A #8-B | Chapter 8: Counters | Assignment #7 (Chapter 8) | Not Hand in |
| Dec. 27 (W) | #9-A | Chapter 9: Shift Registers | Assignment #8 (Chapter 9) | Not Hand in |
| TBA | | Final Exam | | |

Course Web Site

The Course Web Site contains materials used in the class and other related materials to the course

Location:

<https://mylearningspace.wlu.ca/>

Follow the link for PC220